TC7S14F/FU

TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# TC7S14F, TC7S14FU

#### **SCHMITT INVERTER**

The TC7S14 is a high speed  $C^2MOS$  SCHMITT INVERTER fabricated with silicon gate  $C^2MOS$  technology. It achieves a high speed operation similar to equivalent LSTTL while maintaining the  $C^2MOS$  low power dissipation.

Pin Configuration and function are the same as the TC7SU04F but input have 25% V<sub>CC</sub> hysteresis and with its schmitt trigger function, the TC7S14F can be used as line receivers which will receive slow input signal. Input is equipped with protection circuits against static discharge or transinent excess voltage.

Output currents are 1/2 compared to TC74HC series

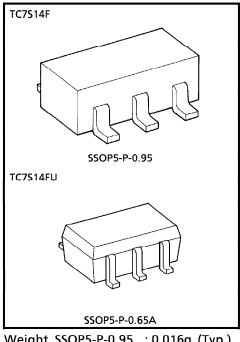
#### **FEATURES**

models.

•	High Speed	$t_{pd} = 11ns (lyp.)$ at $V_{CC} = 5V$
•	Low Power Dissipation	$I_{CC} = 1\mu A$ (Max.) at $Ta = 25^{\circ}C$
•	High Noise Immunity	$V_{H} = 1.1V \text{ at}$

 $V_{CC} = 5V$ 

■ Wide Operating Voltage Range ... V<sub>CC (opr)</sub> = 2~6V

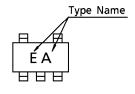


Weight SSOP5-P-0.95 : 0.016g (Typ.) SSOP5-P-0.65A : 0.006g (Typ.)

#### **MAXIMUM RATINGS**

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage Range	Vcc	-0.5~7	V
DC Input Voltage	V <sub>IN</sub>	-0.5~V <sub>CC</sub> +0.5	V
DC Output Voltage	VOUT	-0.5~V <sub>CC</sub> +0.5	<b>V</b>
Input Diode Current	ΙΚ	± 20	mA
Output Diode Current	lok	± 20	mA
DC Output Current	IOUT	± 12.5	mΑ
DC V <sub>CC</sub> /Ground Current	Icc	± 50	mΑ
Power Dissipation	PD	200	mW
Storage Temperature	T <sub>stg</sub>	<b>- 65∼150</b>	°C
Lead Temperature (10s)	ΤL	260	°C

#### MARKING



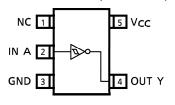
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#### **LOGIC DIAGRAM**



#### PIN ASSIGNMENT (TOP VIEW)



#### TRUTH TABLE

Α	Υ
L	Τ
Ι	L

#### RECOMMENDED OPERATING CONDITIONS

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	۷сс	2~6	V
Input Voltage	VIN	0~V <sub>CC</sub>	V
Output Voltage	VOUT	0~V <sub>CC</sub>	V
Operating Temperature	Topr	- 40~85	°C

#### DC ELECTRICAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	TEST CONDITION			Ta = 25°C			Ta = -4	UNIT	
CHARACTERISTIC	STIVIBUL			Vcc	MIN.	TYP.	MAX.	MIN.	MAX.	CIVIT
Positive				2.0	1.0	1.25	1.5	1.0	1.5	
Threshold Voltage	V <sub>P</sub>		_	4.5	2.3	2.7	3.15	2.3	3.15	V
Tilleshold Voltage				6.0	3.0	3.5	4.2	3.0	4.2	
Negative				2.0	0.3	0.65	0.9	0.3	0.9	
Threshold Voltage	V <sub>N</sub>		_	4.5	1.13	1.6	2.0	1.13	2.0	V
Tilleshold Voltage				6.0	1.5	2.3	2.6	1.5	2.6	
				2.0	0.3	0.6	1.0	0.3	1.0	
Hysteresis Voltage	VΗ		_	4.5	0.6	1.1	1.4	0.6	1.4	V
				6.0	0.8	1.2	1.7	0.8	1.7	
	Vон	V <sub>IN</sub> = V <sub>IL</sub>	I <sub>OH</sub> = -20μA	2.0	1.9	2.0	—	1.9	_	
High Lovel				4.5	4.4	4.5	—	4.4	_	
High-Level				6.0	5.9	6.0	<u> </u>	5.9	_	V
Output Voltage			$I_{OH} = -2mA$	4.5	4.18	4.31	_	4.13	_	
			$I_{OH} = -2.6 mA$	6.0	5.68	5.80	_	5.63	_	
				2.0	<b> </b>	0.0	0.1	_	0.1	
Lavy Lavyal	VOL	   V <sub>IN</sub> = V <sub>IH</sub>	$I_{OL} = 20 \mu A$	4.5	<del> </del>	0.0	0.1	_	0.1	
Low-Level				6.0	_	0.0	0.1		0.1	V
Output Voltage	-	''' '''	$I_{OL} = 2mA$	4.5	—	0.17	0.26	-	0.33	
			I <sub>OL</sub> = 2.6mA	6.0	l —	0.18	0.26	_	0.33	
Input Leakage Current	ΙΝ	V <sub>IN</sub> = V <sub>CC</sub> o	or GND	6.0			± 0.1		± 1.0	μΑ
Quiescent Supply Current	<sup>l</sup> cc	V <sub>IN</sub> = V <sub>CC</sub> or GND		6.0			1.0	_	10.0	μΑ

Output currents are 1/2 compared to TC74HC series models.

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 The information contained herein is subject to change without notice.

### AC ELECTRICAL CHARACTERISTICS ( $C_L = 15pF$ , $V_{CC} = 5V$ , Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	Т	UNIT		
CHARACTERISTIC	STIVIBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNII
Output Transition	<sup>t</sup> TLH			1	Q	
Time	tTHL	_		4	٥	nc
Propagation Delay	t <sub>pLH</sub>			11	21	ns
Time	$t_{pHL}$	_		''	41	

### AC ELECTRICAL CHARACTERISTICS ( $C_L = 50pF$ , Input $t_r = t_f = 6ns$ )

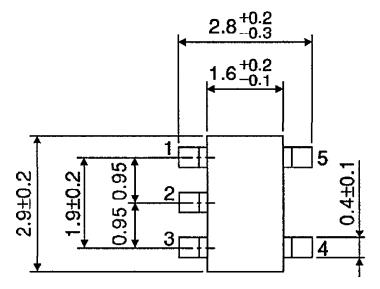
CHARACTERISTIC	SYMBOL	TEST CONDITION		Ta = 25°C			Ta = -4	UNIT	
CHARACTERISTIC		TEST CONDITION		MIN.	TYP.	MAX.	MIN.	MAX.	ONIT
Output Transition	4		2.0	_	50	125	_	145	
Time	<sup>t</sup> TLH	_	4.5	<b> </b> —	14	25	<b> </b>	30	
Time	<sup>t</sup> THL		6.0	—	12	21	—	24	
Dramagation Dalay	4		2.0	_	48	100	_	235	ns
Propagation Delay Time	t <sub>pLH</sub>	<u> </u>	4.5	<del> </del>	12	20	<del> </del>	48	
rime	t <sub>pHL</sub>		6.0	—	9	17	—	40	
Input Capacitance	CIN	_		_	5	10	_	10	
Power Dissipation Capacitance	C <sub>PD</sub>	Note (1)		_	28	_	_	_	pF

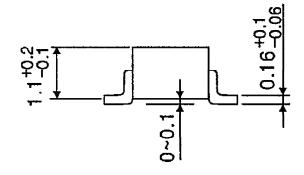
Note (1) :  $C_{\mbox{\scriptsize PD}}$  is defined as the value of internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation : ICC (opr) = CPD·VCC·fIN + ICC

# OUTLINE DRAWING

SSOP5-P-0.95





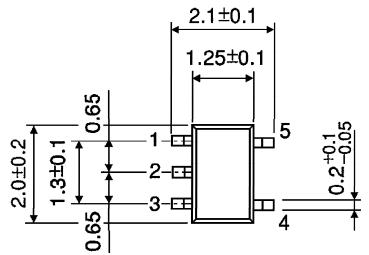


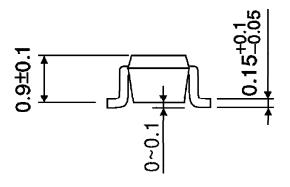
Weight: 0.016g (Typ.)

Unit: mm

## **OUTLINE DRAWING**

SSOP5-P-0.65A





Weight: 0.006g (Typ.)

# **Mouser Electronics**

**Authorized Distributor** 

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